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STATEMENT BY APPLICANT**

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Complete if Known

Application Number	09/994,982
Filing Date	11/26/2001
First Named Inventor:	Xia Dei
Art Unit	2116
Examiner Name	Yanchus III, Paul B.
Attorney Docket Number	42P9724X

Sheet	1	of	1
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		us 6,941,480 B1	09-06-2005	Dai	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^o
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		JP 11-045658	01-22-1999	Matsushita Electric Ind Co Ltd		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
JB		BELLAOUAR, A., et al., "Supply Voltage Scaling for Temperature Insensitive CMOS Circuit Operation", 1057-7130/98 Copyright 1998 IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pages 415-417.	
JB		DANCY, Abram, et al., "Techniques for Aggressive Supply Voltage Scaling and Efficient Regulation", 0-7803-3669-0 Copyright 1997 IEEE, IEEE 1997 Custom Integrated Circuits Conference, page 579-586.	
JB		GONZALEZ, Ricardo, et al., "Supply and Threshold Voltage Scaling for Low Power CMOS", 0018-9200/97 Copyright 1997 IEEE, IEEE Journal of Solid-State Circuits, Vol. 32, No. 8, August 1997, pages 1210-1216.	

Examiner Signature		Date Considered	7/14/06
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English Translation is attached.

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